

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (Currently Amended) A [[Data]] data conversion device, intended to work on primary elementary data items individually coded according to a first arrangement of words, comprising ~~characterized in that it comprises:~~

[[\*]] storage means ~~[[ (7 ) ] ]~~ for storing a first set of symbols, all different, forming a representation of ~~[[ the ] ]~~ said first arrangement and a second set of symbols, all different, forming a representation of a second arrangement of words~~[[ , ] ]~~; and

[[\*]] an operator ~~( 8 ) devised so as~~ configured to receive as input a primary elementary data item, as well as ~~the~~ said first and second sets of symbols, and ~~so as to perform on~~ ~~[[ this ] ]~~ the primary elementary data item, word transformations defined solely by the said first and second sets of symbols in such a way as to output a corresponding secondary data item equivalent to the said primary elementary data item, wherein the first and second sets of symbols are ordered strings of numbers used for processor or microprocessor words.

2. (Currently Amended) ~~Device according to Claim 1~~ A data conversion device, intended to work on primary elementary data items individually coded according to a first arrangement of words, comprising:

storage means for storing a first set of symbols, all different, forming a representation of said first arrangement and a second set of symbols, all different, forming a representation of a second arrangement of words;

an operator configured to receive as input a primary elementary data item, as well as said first and second sets of symbols, and to perform on the primary elementary data item, word transformations defined solely by said first and second sets of symbols in such a way as to output a corresponding secondary data item equivalent to said primary elementary data item, in which a first item of equipment [[ ( 1 ) ] ] ~~delivers the~~ is configured to deliver said primary elementary data items coded according to said first arrangement, and a means of conversion ~~[[ ( 5 ' ) ] ] delivers~~ is configured to deliver secondary data items coded according to a third arrangement, after conversion of primary elementary data items coded according to a fourth arrangement by a second item of equipment ~~[[ ( 1 ' ) ] ]~~, ~~[[ the ] ]~~ said items of equipment

configured (1, 1') ~~desiring~~ to exchange primary elementary data items,

~~characterized in that the~~ wherein said operator  $[(8)]$  comprises means of interrogation  $[(9)]$  ~~devised~~ configured so as:

$[[*]]$  to supply ~~the~~ said second item of equipment  $[(1')]$  with a message containing  $[[the]]$  said second set of symbols and requiring the sending back to  $[[the]]$  said operator  $[(8)]$  of a primary elementary data item, transform of  $[[the]]$  said second set of symbols by coding according to  $[[the]]$  said fourth arrangement,

$[[*]]$  ~~deducing to deduce~~ from  $[[this]]$  said primary elementary data item as well as from the first and second sets of symbols a third set of symbols forming a representation of ~~the~~ said fourth arrangement,

$[[*]]$  ~~replacing to replace~~  $[[the]]$  said second set of symbols by  $[[the]]$  said third set of symbols, in  $[[the]]$  said operator  $[(8)]$  and in  $[[the]]$  said means of conversion  $[(5')]$ , so that in the event of the transmission of a primary elementary data item coded according to the first, respectively fourth, arrangement and intended for  $[[the]]$  said second, respectively first, item of equipment, ~~the~~ said operator  $[(8)]$  delivers to the latter, directly, a primary elementary data item coded according to the fourth, respectively first arrangement.

3. (Currently Amended) The Device device according to Claim 2, ~~characterized in that the~~ wherein said first, second and third sets of symbols are ordered strings of numbers.

4. (Currently Amended) The Device device according to Claim 3, ~~characterized in that the~~ wherein said second set of symbols is the string  $[1, 2, 3, \dots, n-1, n]$ ,  $n$  being the number of components of a base over which the secondary data item is decomposed into words of  $k$  bits,  $k$  being greater than or equal to 1, ~~and in particular equal to 8~~.

5. (Currently Amended) The Device device according to any one of Claims 2 to 4, ~~characterized in that the~~ wherein said second and third arrangements are identical.

6. (Currently Amended) The Device device according to any one of Claims 2 to 4, wherein ~~characterized in that the~~ said second and third arrangements are different and are associated with sets of symbols comprising numbers of words which are different  $[[and/or]]$  or words of number of bits which are different, and ~~in that the~~ wherein said means of interrogation are devised so as to address to  $[[the]]$  said means of conversion  $[(5')]$  at least

one second set of symbols so that ~~[[it]]~~ the second set of symbols is substituted for ~~[[the]]~~ said third arrangement.

7. (Currently Amended) The Device ~~device~~ according to Claim 6, wherein ~~characterized in that the~~ said storage means ~~[[ (7) ]]~~ store ~~stores~~ several second sets of symbols comprising numbers  $n_i$  of words which are different from each other, or and/or of words of number  $k_i$ ; of bits which are different from each other, ~~and in that the~~ wherein said means of interrogation ~~[[ (9) ]]~~ are devised so as to supply ~~the~~ said second item of equipment ~~[[ (1' ) ]]~~, via a message, with a chosen number of first sets of symbols which are different from each other, where i corresponds to the ith second set.

8. (Currently Amended) The device ~~Device~~ according to ~~one of Claims~~ claim 2 ~~to 7~~, in which ~~the~~ said first item of equipment ~~[[ (1) ]]~~ is stored in a first machine ~~[[ (M1) ]]~~, ~~in particular a computer, characterized in that the~~ wherein said storage means ~~[[ (7) ]]~~ and ~~a part at least of the~~ at least a portion of said operator (8, 9-1) are installed in ~~the~~ said first machine.

9. (Currently Amended) The device ~~Device~~ according to Claim 8, ~~characterized in that the~~ wherein said storage means ~~[[ (7) ]]~~ and ~~a part (8, 9-1) at least of the~~ at least a portion of said operator are installed in the form of a program in ~~[[the]]~~ said first machine ~~[[ (M1) ]]~~.

10. (Currently Amended) The device ~~Device~~ according to ~~one of Claims~~ claim 8 and 9, ~~in which the~~ wherein said second item of equipment ~~[[ (1' ) ]]~~ and ~~[[the]]~~ said means of conversion ~~[[ (5) ]]~~ are installed in a second machine ~~[[ (M2) ]]~~, ~~in particular a computer, characterized in that~~ a first part ~~[[ (9-1) ]]~~ of ~~[[the]]~~ said interrogation means is installed in ~~the~~ said first machine, while a complementary second part ~~[[ (9-2) ]]~~ is installed in ~~the~~ said second machine ~~[[ (M2) ]]~~.

11. (Currently Amended) The device ~~Device~~ according to Claim 10, ~~characterized in that the~~ wherein said means of interrogation ~~[[ (9-2) ]]~~ are installed in the form of a program.

12. (Currently Amended) The device ~~Device~~ according to Claim 11, ~~characterized in that the~~ wherein said operator ~~[[ (8) ]]~~ is ~~devised~~ configured so as to install ~~[[the]]~~ said second part ~~[[ (9-2) ]]~~ of the means of interrogation in ~~[[the]]~~ said second machine ~~[[ (M2) ]]~~ when ~~[[the]]~~ said first item of equipment attempts, for the first time, to exchange primary

elementary data items with [[the]] said second item of equipment [[(1')]].

13. (Currently Amended) ~~A process~~ Process for converting primary elementary data items individually coded according to a first arrangement of words, ~~characterized in that it comprises the following steps comprising:~~

a) providing a first set of symbols, all different, forming a representation of [[the]] said first arrangement and a second set of symbols, all different, forming a representation of a second arrangement of words [[and,]]; and

b) receiving a primary elementary data item, as well as [[the]] said first and second sets of symbols[[,]]; and

c) performing on [[this]] said primary elementary data item, word transformations defined solely by [[the]] said first and second sets of symbols in such a way as to output a corresponding secondary data item equivalent to [[the]] said primary elementary data item, wherein the first and second sets of symbols are ordered strings of numbers used for processor or microprocessor words.

14. (Currently Amended) ~~Process according to Claim 13~~ A process for converting primary elementary data items individually coded according to a first arrangement of words, comprising:

a) providing a first set of symbols, all different, forming a representation of said first arrangement and a second set of symbols, all different, forming a representation of a second arrangement of words;

b) receiving a primary elementary data item, as well as said first and second sets of symbols; and

c) performing on said primary elementary data item, word transformations defined solely by said first and second sets of symbols in such a way as to output a corresponding secondary data item equivalent to said primary elementary data item, in which a primary elementary data item coded according to the said first arrangement is received from a first item of equipment and a secondary data item coded according to a third arrangement is received from a means of conversion, the [[latter]] secondary data item arising from the conversion of a primary elementary data item coded according to a fourth arrangement by a second item of equipment,

~~characterized in that in~~ wherein step b) comprises:

[[\*]] supplying ~~the~~ said second item of equipment ~~is supplied~~ with a message containing ~~the~~ said second set of symbols and requiring the sending back of a primary elementary data item, transform of ~~the~~ said first set of symbols by coding according to ~~the~~ said fourth arrangement, ~~then~~;

[[\*]] deducing a third set of symbols forming a representation of ~~the~~ said fourth arrangement ~~is deduced~~ from ~~this~~ said primary elementary data item as well as from the first and second sets of symbols, and

[[\*]] replacing ~~the~~ said second set of symbols ~~is replaced~~ everywhere by ~~the~~ said third set of symbols, so that in the event of the transmission of a primary elementary data item coded according to the first, respectively fourth, arrangement and intended for ~~the~~ said second, respectively first, item of equipment, a primary elementary data item coded according to the fourth, respectively first arrangement is delivered directly to the latter item of equipment.